Docket No.: 57454-963 **PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Te Application of

Customer Number: 20277

Hiroshi Maeda, et al.

Confirmation Number: 5440

Serial No.: 10/633,576

Group Art Unit: 2811

Filed: August 05, 2003

Examiner:

For:

SEMICONDUCTOR DEVICE WITH CAPACITOR ELECTRODES AND METHOD OF

MANUFACTURING THEREOF

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the document listed on the attached form PTO-1449. It is respectfully requested that the document be expressly considered during the prosecution of this application, and that the document be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

10/633,576

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: November 26, 2003

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APPLICATION (PTO 1449)					APPLICANT Hiroshi Maeda, e	APPLICANT Hiroshi Maeda, et al.					
(PTO-1449)					FILING DATE August 05, 2003	FILING DATE GROUP 2811					
				U.S. PATE	NT DOCUMENTS	*			_	*	
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.